**PATENT** 

# DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

the specification of which: (check one)

My residence, post office address and citizenship are as stated below next to my name;

I believe I am an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### VHDL TECHNOLOGY LIBRARY METHOD FOR EFFICIENT CUSTOMIZATION OF CHIP GATE DELAYS

XXX	is attached hereto.			
	as Application Serial N	ket Number No	ک پید کے بید کے بید ک	
I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.				
	wledge the duty to dis ance with 37 CFR 1.56.		h is material to th	e patentability of this application in
I hereby claim the benefit of foreign priority under 35 USC 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application the priority of which is claimed:				
Prior Fo	oreign Application(s):			Priority Claimed
(Numbe	er) (Co	ountry)	(Filing Date)	Yes <u>X</u> No
I hereby claim the benefit of United States priority under 35 USC 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in a listed prior United States application in the manner provided by the first paragraph of 35 USC 112, I acknowledge the duty to disclose information material to the patentability of this application as defined in 37 CFR 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:				
(Applica	ation Serial #)	(Filing Date)		(Status)
				re true and that all statements made se statements were made with the

patent issued thereon.

knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 USC 1001 and that such willful false statements may jeopardize the validity of the application or any

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Lynn L. Augspurger Floyd A. Gonzalez Christopher A. Hughes Stanley B. Green John E. Hoel Lawrence D. Cutter William A. Kinnaman, Jr. William B. Porter Andrew J. Wojnicki, Jr.	Reg. No. 24,227 Reg. No. 26,732 Reg. No. 26,914 Reg. No. 24,351 Reg. No. 26,279 Reg. No. 28,501 Reg. No. 27,650 Reg. No. 33,135 Reg. No. 43,995	Martin Fleit Robert C. Kain Jon A. Gibbons Jose Gutman Stephen Bongini William R. Trueba, Jr. Jeffrey Giunta Mark Terry	Reg. No. 16,900 Reg. No. 30,648 Reg. No. 37,333 Reg. No. 35,171 Reg. No. 40,917 Reg. No. 48,002 Reg. No. 42,583 Reg. No. 47,133
Andrew J. Wojnicki, Jr. Joseph C. Redmond, Jr.	Reg. No. 43,995 Reg. No. 18,753		
Joseph C. Neumona, Jr.	116g. 110. 10,133		

Direct all telephone calls to Jose Gutman (561) 989-9811 and all correspondence to Customer Number 23334



FULL NAME OF INVENTOR: Marvin J. RICH

INVENTOR'S SIGNATURE: Manin, Ruch DATE: 12 27 C

RESIDENCE: 7 Bird Lane, Poughkeepsie, NY 12603

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POST OFFICE ADDRESS: Same as above

FULL NAME OF INVENTOR: Ashutosh MISRA

INVENTOR'S SIGNATURE:\_\_\_\_\_DATE:\_\_\_\_

RESIDENCE: 3604, 13th 'G' Main, 5th Cross, H.A.L. II Stage, Bangalore, Karnataka State, 560008, India

CITIZENSHIP: India

POST OFFICE ADDRESS: Same as above

#### **EXPRESS MAIL LABEL NO. EL863783632US**

**PATENT** 

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### VHDL TECHNOLOGY LIBRARY METHOD FOR FFFICIENT CUSTOMIZATION OF CHIP GATE DELAYS

the spe	acification of which:	(check one)			
XXX	is attached hereto				
1.0	under Attorney's E	Pocket Number rial No I on	(if applicable).		
		viewed and understa y any amendment ref		bove identified specification, Include	din
	owledge the duty to ance with 37 CFR 1		which is material to	the patentability of this applicatio	n I
invento	or's certificate listed	below and have also i	der 35 USC 119 of ar dentified below any fore application the priority	ny foreign application(s) for pater eign application for patent or invent of which is claimed:	it c tor'
Prior F	oreign Application(s	s):		Priority Claimed	
(Numb	er)	(Country)	(Filing Date)	YesXNo	
I hereby claim the benefit of United States priority under 35 USC 120 of any United States application(s) liste below and, insofar as the subject matter of each of the claims of this application is not disclosed in a liste prior United States application in the manner provided by the first paragraph of 35 USC 112, I acknowledg the duty to disclose information material to the patentability of this application as defined in 37 GFR 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of the application:					
(Applic	ation Serial #)	(Filing Date	)	(Status)	•
on info knowle under t	rmation and belief dge that willful false	are believed to be to statements and the l	rue; and further that thickes or made are punish	are true and that all statements m nese statements were made with nable by fine or imprisonment, or b ize the validity of the application or	th oth

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#### EXPRESS MAIL LABEL NO. EL863783632US

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